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PATENTS
ALT-155

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Tony K. Ngai et al.
Application No.: 09/124,649
Filed : July 29, 1998
For : PROGRAMMABLE LOGIC DEVICE HAVING
EMBEDDED DUAL-PORT RANDOM ACCESS MEMORY
CONFIGURABLE AS SINGLE-PORT MEMORY
Group Art Unit : 2751

New York, New York 10020
October 26, 1998

Hon. Assistant Commissioner
for Patents
Washington, D.C. 20231

Attention: Box MISSING PARTS

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, applicants
hereby make the following publications of record in the
above-identified patent application:

Actel Corporation, "Integrator Series FPGAs -
1200XL and 3200DX Families" data sheet, September 1997

Lucent Technologies Inc., ORCA™ OR2CxxA (5.0 V)
and OR2TxxA (3.3 V) Series Field-Programmable Gate
Arrays" data sheet, August 1996

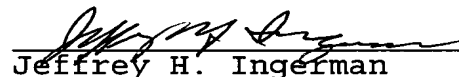
Xilinx, Inc., "XC4000E and XC4000X Series Field
Programmable Gate Arrays" data sheet, Version 1.4,
November 10, 1997

Copies of the aforementioned publications, which are listed on the accompanying Form PTO-1449 (submitted in duplicate), are enclosed herewith.

It is respectfully requested that these patents be (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicant requests that a copy of Form PTO-1449, as considered and initialled by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

Respectfully submitted,



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